EXPRESS MAIL LABEL NO.: EV 062338703 US ATTORNEY DOCKET NO.: N1085-00192 [TSMC 2003-0289]

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Chuan-Ping Hou et al.

Serial No.: Not Known Yet

Group Art Unit: Not Known Yet

Filed: 11/24/2003

Examiner: Not Known Yet

For: METHOD OF FORMING FIELD EFFECT TRANSISTOR

I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as "Express Mail Post Office to Addressee" Mailing Label Number EV 062338703 US to Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

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Date

lose h A. Powers, Reg. No. 47,006

INFORMATION DISCLOSURE STATEMENT TRANSMITTAL LETTER

Dear Sir:

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Enclosed herewith is an Information Disclosure Statement pursuant to 37 CFR. § 1.56 in connection with the above-identified application, which statement is being filed:

- [X] Together with the present application.
- [] Before the first Office Action on the merits or three (3) months from the filing date of this application, whichever occurs last. [37 CFR § 1.97(b)]
- [] After the first Office Action on the merits, but before a Final Office Action under §1.113 or Notice of Allowance under §1.311, whichever occurs first. [37 CFR § 1.97(c)]
- [] After a Final Office Action under §1.113 or Notice of Allowance under §1.311, but prior to or with payment of the Issue Fee. [37 CFR § 1.97(d)]

Consistent with Applicant's obligations pursuant to 37 CFR §§1.97 and 1.98, the following requirements have been met:

- [x] No separate requirements are needed.
- [x] No additional fee is required.

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[] Fee Under 37 CFR § 1.97(c)

The fee of \$180.00 for submission of an IDS under § 1.97(c) as set forth in § 1.17(p) accompanies this statement.

[] Fee Under 37 CFR § 1.97(d)

The fee of \$180.00 for submission of an information disclosure statement under § 1.97(d) set forth in § 1.17(p) accompanies this statement.

[] Certification Under 37 CFR § 1.97(e)

- () Each item of information contained in this statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement; or
- () No item of information contained in this statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the statement after making reasonable inquiry, no item of information contained in this statement was known to any individual designated in §1.56(c) more than three months prior to the filing of this statement.

Provision of Copies of References

[X]	A copy of each of the non-U.S. patent references listed on the attached Form PTO-1449 is enclosed herewith and forms a part hereof.						
	[].	Partial Translations of References form a part hereof.	are enclosed herewith and				
	[]	A copy of the European Search Report from a EPO application is enclosed herewith.	corresponding or related				
	[]	A copy of the International Search Report from PCT application is enclosed herewith.	n a corresponding or related				

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Identification of Prior Application(s) In Which Listed Information Was Already Cited And For Which No Copies Are Submitted Or Need Be Submitted

- [] This application relies, under 35 U.S.C. § 120, on the earlier filing date of prior U.S. Application No(s).

 The following references were submitted to, and/or cited by, the Office in the prior application(s) and therefore are not required to be provided in this application:
- [x] The Commissioner is hereby authorized to charge any fees associated with this communication or credit any overpayment to Deposit Account No. <u>04-1679</u>. A duplicate copy of this transmittal is attached.

Respectfully submitted,

DATE: 11 24 03

Joseph A. Powers Registration No. 47,006 Attorney for Applicant(s)

DUANE MORRIS LLP One Liberty Place Philadelphia, PA 19103-7396 Telephone: 215-979-1842

Fax: 215-979-1020

PTO/SB/08A (04-03)

Approved for use through 04/30/2003. OMB 0651-0031

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Complete if Known Substitute for form 1449/PTO Application Number Not Yet Assigned Filing Date 11/24/2003 INFORMATION DISCLOSURE First Named Inventor Chuan-Ping Hou STATEMENT BY APPLICANT Art Unit Not Yet Assigned (Use as many sheets as necessary) **Examiner Name** Not Yet Assigned Sheet 1 Attorney Docket Number N1085-00192

				DOCUMENTS	
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ^{2 (f known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevan Figures Appear
-	Α	US- 6,413,802	07-02-2002	Hu et al.	3
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FOREIGN PATENT DOCUMENTS								
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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Sheet	2	of	2	Attorney Docket Number	N1085-00192		

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	В	FU-LIANG YANG ET AL., "25 nm CMOS Omega FETs", 2002 IEEE, 4 pages	
	С	S. MONFRAY ET AL., "50 nm - Gate All Around (GAA) - Silicon On Nothing (SON) - Devices: A Simple Way to Co-integration of GAA Transistors within bulk MOSFET	
		process", 2002 IEEE, 2002 Symposium on VLSI Technology Digest of Technical Papers, 2 pages	
	D	J.P. COLINGE ET AL., "SOI Devices for Sub-0.1 um Gate Lengths", 2002 IEEE, 2002 Proc. 23rd International Conference on Microelectronics (Miel 2002), Vol 1, 2 pages	
	E	FU-LIANG YANG ET AL., "35 nm CMOS FinFETs", 2002 IEEE, 2002 Symposium on VLSI Technology Digest of Technical Papers, 2 pages	
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